California AHMCT Program University of California at Davis California Department of Transportation

Control of a Continuously Variable Transmission for Variable Geometry Mobile Equipment

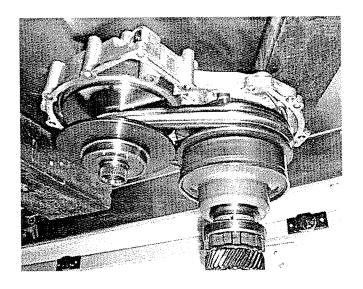
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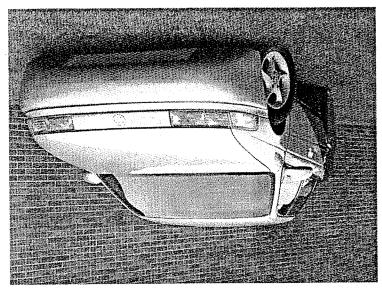
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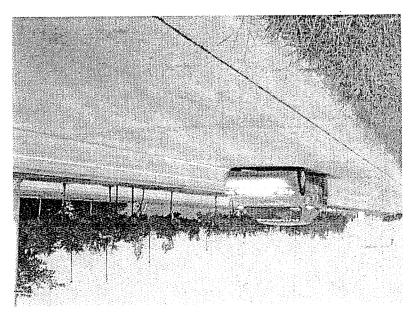


Table of contents:

- 0.0 Abstract
- Phase I
- 1.0 Introduction
- 2.0 Setup
- 3.0 Procedure
- 4.0 Results
- 4.1 Mean Power Draw
- 4.2 Clamping Pressure Control Algorithm Development
- 4.3 Shift System Control Algorithm Development
- 4.4 Power Control Theory and Development
- Phase II
- 5.0 Introduction
- quia2 0.0
- 7.0 Results
- 8.0 Conclusion
- 9.0 References
- A.0 Appendix A, Sample Pressure Control Algorithm
- B.0 Appendix B, Sample Shift Control Algorithm
- C.0 Appendix C, Specification of Servomotors and Pumps for Control of CVT
- D.0 Appendix D, Derivation of CVT Governing Equations

Phase 1 0.0 Abstract

The purpose of this two year endeavor was to design, construct and demonstrate the servo-hydraulic control concepts proposed by Gear Chain Industrial and Dr. Andrew Frank for the control of a Continuously Variable Transmission (CVT).

In Phase I of the project, a 1.0L class Nissan CVT was fitted with a servo-hydraulic system under microprocessor control. The assembly was then experimentally characterized and closed loop compensators were developed to improve system response. Mon-linear

system under microprocessor control. The assembly was then experimentally characterized and closed loop compensators were developed to improve system response. Non-linear characteristics of the system made compensator design difficult, and techniques for the linearization of the system by feedback control were explored. A control system is proposed and demonstrated.

Phase II of the project involved the implementation of a 2.0L class CVT into a vehicle for evaluation and as a proof of concept. The vehicle chosen was the 1999 UC Davis Future Car and the CVT was an integral part of the parallel CVT Hybrid powertrain. The CVT proved to help increase efficiency while maintaining excellent driveability.

1.0 Introduction

In an effort to achieve the highest possible fuel economy in a hybrid-electric vehicle powertrain, UC Davis began researching the application of a Continuously Variable Transmission (CVT). This transmission could be used to precisely control the speed and torque on the powertrain through the continuous selection of gear ratio. This technology would then allow the vehicle powertrain to operate at its most efficient point for each power demand. By increasing the vehicle power efficiency in this way, it is possible to approach the highest theoretical fuel economy in a real mechanical system. The demonstration in a vehicle platform can easily be extended to other mechanical equipment requiring power transmission.

Up to this point in time, UC Davis had assembled a prototype servo-hydraulic CVT control system on a dynamometer. This paper explores the replacement of these motors with new, light weight and highly efficient DC brushless motors, the development of the control system compensators necessary precisely control a CVT, and explores the control concepts necessary to make CVTs a reality for electric power assist dual power plant vehicles and industrial equipment.

quis2 0.2

Figure 3.1 depicts the hardware setup on the dynamometer pad. The CVT is a 1.0L class Missan transmission and is mounted on a stand with power introduced via the large electric motor in the back of the room. Power flows from the motor, through the CVT to a belt and pulley system, and then to the Eddy current dynamometer.

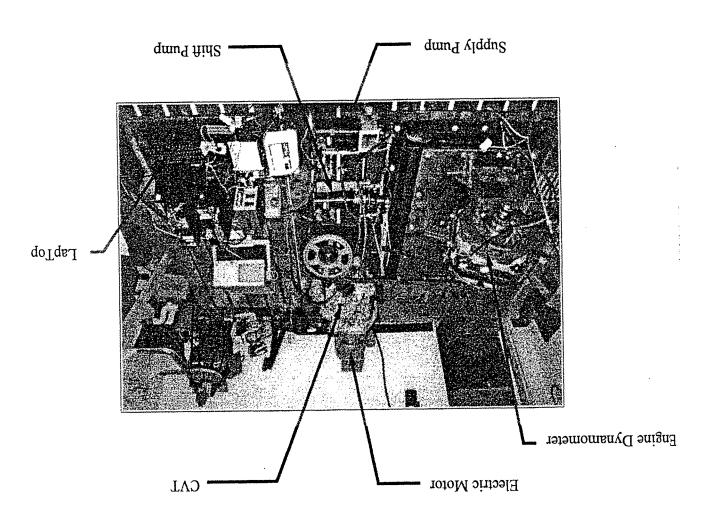


Figure 3.1 Dynamometer setup for CVT testing.

The ratio and clamping pressure of the CVT are controlled through the servo-pumps which are interfaced to a microprocessor. The processor is programmed via the Laptop in the bottom right of the picture.

Gear Chain Industrial and Prof. A. Frank inspired this research with the paper <u>A New</u> traditional mechanical control valve assembly and line pressure pump with the servo-hydraulic system shown in Figure 3.2.

Servo motors 1 & 2 drive small gear pumps which generate the pressure and flow of the hydraulic system. Pump 1 connects the two cylinders together. Actuation of this pump moves fluid back and forth between the two cylinders. This fluid movement changes the relative

displacement of the pulley faces, thereby changing the ratio of the transmission. The speed of servo 1 dictates the flow rate of the fluid and thus, the shift rate of the system.

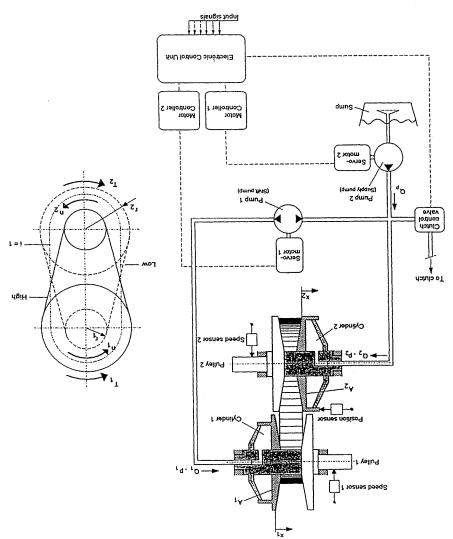


Figure 3.2. Servo-hydraulic setup for Control of CVT

Pump 2 controls the line pressure of the system. The pressure generated by this pump, via cylinders 1 and 2, generates the force necessary to clamp the sides of the CVT belt and transmit torque.

The specification of the pumps and motors used appear in Appendix C.

3.0 Procedure

The CVT control system consists of two groups, the clamping pressure group and the shift group. Each group was characterized in the same way but at different frequency ranges. Each servo amplifier was forced by a function generator introducing a sine wave to the system. The output of the system and the function generator traces were then recorded on a multi-channel data acquisition system. A typical response curve would look something like Figure 4.1.

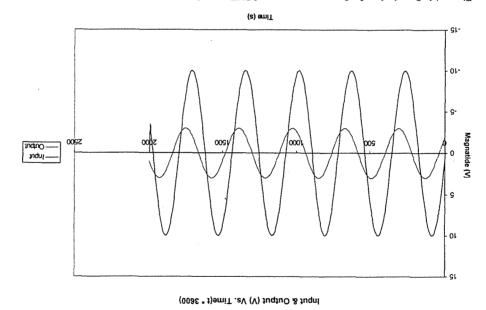


Figure 4.1. Sample data for frequency response of CVT control systems.

The gain and phase lag for each frequency was then recorded to generate the Bode plots analyzed in sections 4.2 and 4.3.

4.0 Results 4.1 Mean Power Draw

The benefits of an electronically controlled CVT are two fold. The first is that the CVT may then be used in a vehicle which does not have an engine idling at all times. Conventionally, a CVT generates primary line pressure via a pump on the main engine shaft. The pump size is such that the engine idling provides the maximum required power and flow rate to the hydraulic system. This means that the pump is oversized for normal operation therefore reducing efficiency. Electric and hybrid-electric vehicles do not need such a continuous supply of mechanical power because, their battery packs a capable of running the servo-motors while the mean power draw of the hydraulic system. A Missan CVT engineer quoted the mean power draw of the hydraulic system. A Missan CVT engineer quoted the mean power hydraulics draws 0.35 kW peak. That is an improvement of 757% of the peak power draw over the previous mean power draw. Mean power draw should be approximately half the peak in the previous mean power draw. Mean power draw should be approximately half the peak in the peak system.

real system. That is an improvement of 757% of the peak power draw over the previous mean power draw.

$P_{eff} = 0.70$	$EM_{eff} = 0.85$	
$12 \cdot p \cdot P \cdot \frac{1}{1} = 0.119 \text{ kW}$	$12 \cdot \text{p1} \cdot \frac{1}{\text{Peff} \cdot \text{EMeff}} = 0.245 \text{ kW}$	nim\l
$11 \cdot p2 \cdot \frac{1}{1} = 0.048 \text{ kW}$	$11 \cdot p1 \cdot \frac{1}{Peff \cdot EMeff} = 0.098 kW$	nim\[0.1=1wol4
P2=17 bar	16d 25=19	

The following table shows the current used by the pressure servomotor at 165V and a fixed ratio for the 1.0L modified CVT test rig.

140.0	160.0	Power (kW)
22.0	\$5.0	(sqmA) I
Ted 71=q	T=35 bar	Voltage = 165

4.2 Clamping Pressure Control Algorithm Development:

Once the mechanical system was built, the first control task was to develop the clamping pressure of the primary pump assembly. The control variable was chosen to by following the pump is proportional to the torque on the pump shaft. The shaft torque is driven by the torque on the pump shaft. The shaft torque is driven by the torque on the electric motor shaft, which is proportional to the current commanded through its windings. Ultimately, line pressure is proportional to the current commanded through its windings. Controller was selected as a "dumb torque drive" type.

Once a control variable was specified, the system was run in "open loop" mode. Meaning that although the current controlled inverter had its own internal feedback controls, there was no feedback between the pressure of the system and the pressure command. This produced a stable that tracked a pressure proportional a command voltage. The system, however, oscillated at low frequencies and rejected disturbances poorly.

To improve system response characteristics, an analog, proportional, feedback controller was designed and implemented using readily available LM741 op-amps. The small DC offset induced by these inexpensive op-amps was not an issue as a small adjustment to the command variable countered this effect.

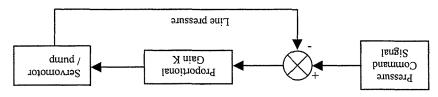


Figure 4.2.1. Analog Controller Block Diagram

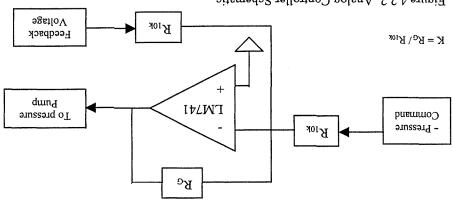


Figure 4.2.2. Analog Controller Schematic

This controller scheme reduced the amplitude of low frequency oscillations of the system and improved disturbance rejection. However, the system suffered from steady state error and did not reject disturbances well enough.

The oscillations of the system were mostly due to a non-linear characteristic of the electric motor and pump near the point of zero velocity. Motor cogging as well and the discontinuity between kinetic and static friction caused the motor shaft to bind whenever the pump shaft velocity went through zero. Figures 4.2.3 and 4.2.4 illustrate this issue.

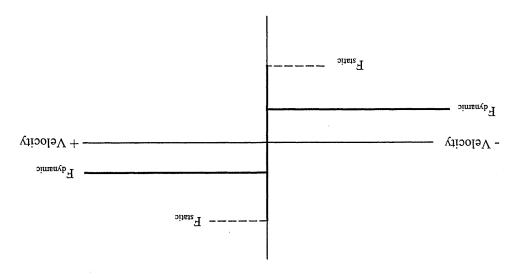


Figure 4.2.3. Monlinear friction Vs. velocity graph

The system would behave as follows: The pressure of the system would start out initially at zero and the command would apply a step input. The error would command the electric motor torque which would accelerate the pump shaft and bring up the system pressure. The system would then reach a point where the feedback pressure and the command signal were the same, yielding an error signal of zero and therefore pump command signal of zero torque. The pump would then stop and its static frictional effects take hold. The system pressure would then slowly fall, effectively ramping the torque signal to the electric motor without actually turning the shaft due to the friction on the shaft. Finally the error signal becomes so large that the shaft brakes free, accelerating to the target pressure due to the now lower kinetic friction. The error then quickly reaches zero and the cycle begins anew.

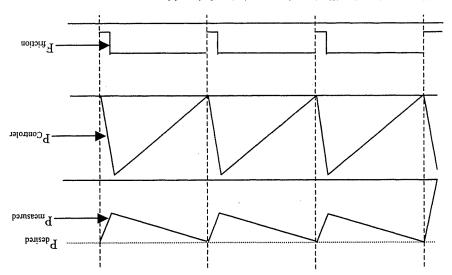


Figure 4.2.4. Oscillating characteristic of closed loop system

Under the advice of a Missan controls engineer, Masahiro Yamamoto, a feed-forward controller was implemented to help alleviate this non-linear effect and reduce the steady state error. The following controls diagram in figure 4.2.5. was implemented with the same inexpensive op-amps used previously.

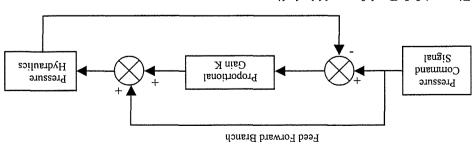


Figure 4.2.5. Feed forward block diagram

This controller introduced a constant command to the servo controller even when the error of the system was zero, thereby eliminating the zero torque command situation. In addition, this system benefited tremendously from the introduction of a deliberate power draw to the system. By placing a bleed from the high pressure side of the system back to the low pressure side a constant flow was introduced to the otherwise nearly static pressure system. This modification kept the pressure pump rotating constantly, effectively avoiding the non-linear zero velocity region. At the typical pressures the system operates at with this low flow rate, it is velocity region. At the typical pressures the system operates at with this low flow rate, it is this bleed could be introduced as a slight allowance in the gear faces.

Due to their increasingly low cost and flexibility the control system was then moved to a digital controller. Programmed in the high level language, C, the digital controller allowed many

```
Figure 4.2.7 Digital controller specification.
                                                              Figure 4.2.6. Sample digital control routine.
                                          Environment
               Dynamic C, Assembler
                                          Development
                178K Elash EEPROM
                          32K SRAM
                                              Memory:
              4 Digital to Analog lines
10 12bit, ±1 lsb Analog to Digital lines
                                                                                                       {
            Oll lines normal digital I/O
                                                                                      return(1);
                                                                       output_to_DAC(output);
       Ol lines high current Digital I/O
                                                                                   clip(output);
                         4 serial ports
                                                   :O/I
                                                                           output = gain * error;
                                          Clock Speed:
                                                                   error = command - feedback;
                            ZHW 81~
                                                                 teeqpack = sample\_teeqpack():
                                                                command = sample_command();
                                 081Z
                                             Processor:
                                                                                     int interrrupt(void){
                             blioW-Z
                                         Manufacturer:
                                                                                 /* Interrupt at 100 Hz */
```

different control algorithms to be tested quickly and easily without changing the hardware. It was, on average, taking one to two days to completely re-wire and test each analog controller design before actually using it to control the system and evaluate system performance. With the digital controller, however, it often took less than one hour to dramatically change the digital control algorithm. In addition, non-linear gain concepts, input and output clipping, and hardware diagnostic services all became the simple task of writing a few lines of code. A sample digital routine and digital controller specification appear in figures 4.2.6 and 4.2.7.

superimposed. The following mathematics would replace the proportional gain K, in figure For example a PID controller would consist of P,1 and D controllers calculated separately and domain via the bilinear transformation or, where possible, Euler's integration and differentiation. All digital control algorithms were developed by moving from the s domain to the z

4.2.1.

Controller_{PID} =
$$Kp + \frac{Ki}{s} + Kd \bullet s$$

 $Kp \Rightarrow P = Kp \bullet Error$
 $\frac{Ki}{s} \Rightarrow I = Ki \bullet \Delta T \bullet Error + I_{n-1}$
 $Kd \bullet s \Rightarrow D = Kd \bullet \left(\frac{Error - Error_{n-1}}{\Delta T}\right)$

Controller Output
$$= P + I + D$$

frequency was chosen to be ten to twenty times the -3dB cutoff frequency of the system on a introduces a lag in the system which leads to oscillations. As a rule, the controller sampling next. On the other hand, if the sampling frequency is too low, the time between samples becomes incapable of measuring the extremely small changes from one sampling period to the frequency. If a frequency too high is chosen, the resolution of the digital to analog converters The first key issue upon moving to a digital controller was the selection of the sampling

A variable frequency sine wave was used to excite the open loop system to determine its bode response curve.

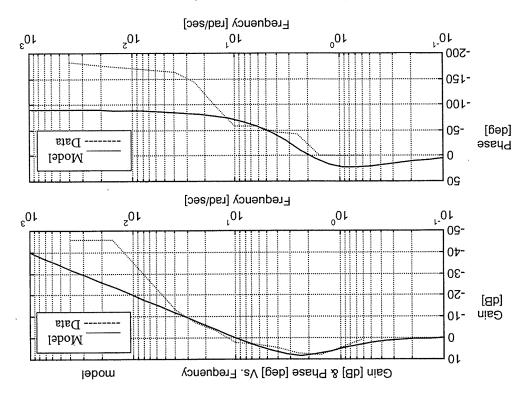


Figure 4.2.8. Model and data for clamping pressure plant

frequency response. Figure 4.2.8 shows the system response data and the model that represents

From this data in figure 4.2.8, a sampling frequency of 100 Hz was selected. This selection proved to be a rather unfortunate error which led to many hours of wrestling with a digital system which would oscillate at about 2 Hz despite it closely approximating a proven analog controller. Although the open loop cutoff frequency of the plant was ~3Hz, the close

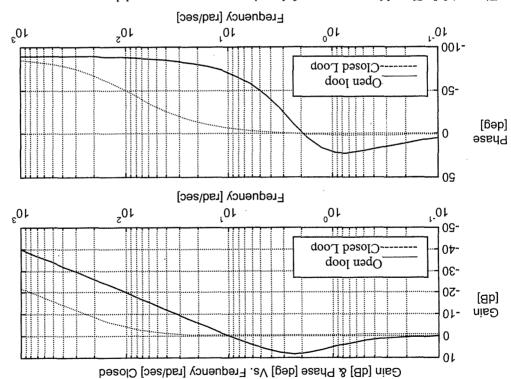


Figure 4.2.9. Closed loop response of clamping pressure system model.

loop frequency response was more like 15Hz, which can be understood by incorporating a proportional controller into the model developed earlier. Figure 4.2.9 shows the model with a proportional controller closing the loop, which demonstrates the higher closed loop frequency response.

Several different control schemes were tested and deemed unusable at 100 Hz. These included PI, ID and PID controllers, all with varying degrees of feed-forward incorporated as schemes before testing even began. I highly recommend further researching these control schemes in the future as the existing system still suffers from steady state error and could improve its disturbance rejection.

The digital, proportional, feed forward controller shown in figure 6 was chosen to control the CVT clamping pressure system. This choice was largely motivated by time constraints but aces work well. There is still room for improvement, however. The disturbances to this system are a direct result of shifting the ratio of the system. Therefore, this known disturbance could also be fed forward to the pressure system to aid in pressure correction.

4.3 Shifting System Control Algorithm Development

After a suitable clamping pressure system was developed, the next step was to develop a system for controlling the ratio of the transmission. For reasons that will be explained in the following section, Developing a CVT Vehicle Control Strategy, it was necessary to control loop rate of change of ratio, dR/dt (R^{\bullet}), in addition to the ratio, R, itself. This separate control loop was never really attempted in analog form, rather, it began as a digitally controlled system. Due to the availability, cost and excellent quality of the Z-World controllers specified above, identical microcontroller hardware shifted the of the CVT.

Vital to the beginning of any compensator design is the selection of the variable to be controlled. Unlike the pressure system, the shift system depends on the displacement of fluid rather than the effort exerted upon the fluid. In a static state, the force exerted by each pulley is constant and the ratio is dependent on how much fluid is contained in each piston cylinder. Moving fluid back and forth between the two cylinders changes the pulley ratio. The shift motor, therefore, operates in velocity mode. Through the constant displacement pump, this mode governs fluid flow rate. Hence, the rate of change of fluid volume in each piston is proportional to the velocity command signal. Figure I shows the mechanical layout of the shifting system.

The same setup used for exciting the pressure system excited the shift system as well. The resulting frequency response of the shift system resulted in the following open loop MATLAB model in figure 4.3.1.

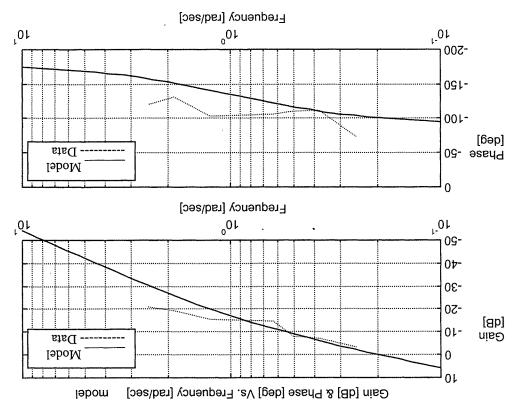


Figure 4.3.1. Model and data for shifting system.

From a closed loop model we get a cutoff frequency of approximately 0.1Hz. This suggests a sampling frequency of 2 Hz would be sufficient for the shift system, however, in light of the previous issues with low sampling rate, 50 Hz was selected as the sampling frequency.

An unfortunate reality is that the feedback variable for the shift system is the ratio R. R can be obtained two ways, by sensing the position of the pulley faces with a linear potentiometer and by dividing the input shaft velocity by the output shaft velocity. By either method, the feedback variable is the integral of the command variable R. This configuration brings two classical control system configurations to mind as shown in figure 4.3.2.

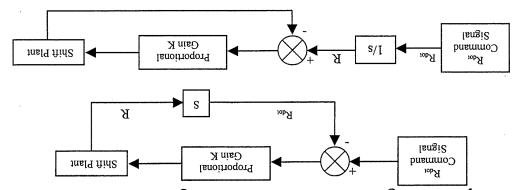


Figure 4.3.2. Two configurations for Rdot control.

From a theoretical standpoint both of these configurations are equally useful, however, each has specific implementation issues that can only be avoided by incorporating the two of them together. The algorithm which integrates the R* suffers from integrator overshoot and poor rate following.

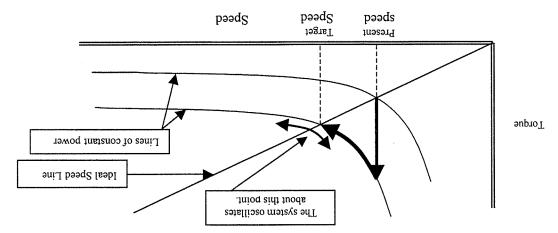


Figure 4.3.3. Shift system oscillations under integral control.

system. Both of these avenues warrant further investigation in the future.

Figure 4.3.3 shows the primary control system commanding a shift rate with the intention of moving down a constant power line to a target point. The system then integrates the commanded R to get a target R. The system then proceeds down the power line with a steady state error. The target point is then reached, and the shift rate command is now zero, however, there is still accumulated R due to the steady state error in the transient portion of the command. So the system overshoots to a point that is too low on the power curve. The cycle now begins anew, only in the opposite direction. The system is too low on the power line, so it commands a negative R which then overshoots to a location too high on the power curve. This overshoot characteristic is a well known effect of integral action upon a control system. The two ways it is characteristic is a well known effect of integral action upon a control system. The two ways it is

classically dealt with are by increasing the proportional gain or adding derivative action to the

The second control configuration suffers from the resolution of the analog to digital converters (ADCs). The ADCs are operating at an 11 bit resolution over a 10V rage. This fixes the maximum resolution to 5mV per bit. With a sampling frequency of 50 Hz, that means that we can accurately detect no less that 0.250V per second of drift. The ratio sensor reads between 0V and 2.5V. The CVT can and does drift from lock to lock in about 10 seconds without this control system detecting it. At low shift rates an accumulator is necessary to make sure correct ratio is tracked as the resolution of the derivative term becomes too low.

The final control system was selected to be a combination of the two systems. At low shift rates, the first control system would accumulate the target ratio. At high shift rates, the ratio signal would be differentiated to provide the most accurate rate control. In addition, there is a provision that resets the integrator in the low shift rate system every time the system command goes through zero. This significantly reduces the effects of integrator induced overshoot. Figure 4.3.4 shows a program that combines all of these features into one program.

```
Figure 4.3.4. Sample R, R' control algorithm.
                                                         teedback[1] = feedback[0];
                                                         // nbqqfe pracory variables
                                                                onfbut_dac(output);
                                                                     cjip (onfbnf):
                                          // Send output to DACs with level checking
                                                      onconc = prap daru * error:
                                                        exxox = command - K qot;
                         .bropse //
// Max command signal is 2.5 V per
 // Scale command variable so that
                                                     command = command \ (50.0);
       // Acknowledge state change
                                                                     ! Z =
                                                                            Scace
     // Kate feed back control here
                                                                              ejse{
                                                    = Jow_gain * error;
                                                                           andano
                                                         = K_target - R;
         // Integrate Rdot command
                                    R_target = R_target + command *sample_time;
                                                                  K_tardet = K;
                                                                  :T =
                                                                         srate
              // Reset accumulator
                                                                  if(state != 1){
      // -command_low < command < 0
                                      if (command < 0 && command > -1*command low) {
                                                       oncbnc = jow_dain * error;
                                                          error = R_target - R;
         // Integrate Rdot command
                                    R_target = R_target + command *sample_time;
                                                                 g_{x} = g_{x} = g_{x}
                    // accumulator.
                                                                 : o =
// If we just changed states, reset
                                                                 }(0 =! =data)li
                                         if (command > 0 && command < command low) {
      // 0 < command < command_low
                                                           // row shift rate command
                                                                    command = 0;
                                  if ( command < low_band && command > -l*low_band)
                               // Allow for a dead band in the command for zero Rdot
                                 Rdot = (feedback[0] - feedback[1]) \ sample_time;
= teeqpsck[0]:
           1/ xange: 0 - 2.5
                                                          teedback[0] = sample_R();
            // xange: ± 2.5
                                                    = sambje_command();
                                                                            command
                                        // Sample the command and feedback variables
                                                                     interrupt (void) {
                                   // Interrupt code body. This code runs at 50 Hz.
```

This algorithm reads the Rdot and R feedback variables from the external analog to digital converters. It then stores the R value read and calculates Rdot using Euler's method of differentiation. All external information has been read and processed.

Next the program decides weather the command is small enough to warrant ignoring the

rate variable calculation. If so, it moves into states 0 or 1. Upon first entering a state, the accumulator R_target is reset. The program then integrates the command variable and tracks a constant ratio. Upon exiting this portion of the routine, the program clips the output variable and sends it to the digital to analog converters.

If the command is large enough to use rate feedback, the command variable is scaled and

the feedback rate subtracted to produce an error for proportional control. The output is then clipped and output to the DACs as with the above code portion.

The simplicity afforded by the digital controller is magnificent. The design of such an analog system would not only be arduous, but it would require extensive work to make simple modifications.

The above code fragment accurately controls the CVT shift rate at high speeds and does not drift at low speeds where rate feedback looses its resolution.

4.4 Power Control Theory and Development

The addition of a CVT to a conventional powertrain allows continuous control of power loading where only discrete control was available before. Taking advantage of this feature evolved a new power management strategy with pitfalls as well as benefits.

First, it becomes necessary to define what powertrain variable we want to control directly

with each of the inputs available to the driver. A conventional car with a manual transmission provides a basis for this analysis, where the driver can control the accelerator, the brakes and the transmission gear selection. Suppose for a moment that a driver wishes to accelerate to 60 mph from a stop. He would engage the engine at a high throttle setting in first gear, commanding engine speed in the gear he would then shift to second gear and continue at his high throttle setting until again the engine approached its maximum speed. And so on, until 60 mph was setting until again the engine approached its maximum speed. And so on, until 60 mph was setting until again the engine approached its maximum speed. At this point, he would cut back on the throttle until the torque output from the engine equaled the road load and aerodynamic losses. He would then continue at a steady state speed of equaled the road load and aerodynamic losses. He would then continue at a steady state speed of

The above logic chain lead to the first power management strategy. This strategy consisted of interpreting the accelerator pedal and brake pedal as 0 to 100% positive and negative torque from the powertrain. A simulation modeling the CVT dynamics and the Federal Urban Driving Cycle (FUDS) was written in the C programming language. The target ratio of the CVT was chosen as a function of the torque commanded. The powertrain would seek the speed at which the torque setting was the most efficient. The simulation performed well and proved that the torque setting was the most efficient. The simulation performed well and failed to show that the driver became a dependent feedback variable as well as a command link is that the driver became a dependent feedback variable as well as a command selection of the gear ratio and the torque of the engine through the really commands power output of the powertrain and not just the torque. The simulation was really commands power output of the powertrain and not just the torque. The simulation was then re-written with this new insight.

The new control routine took the following form in figure 4.4.1:

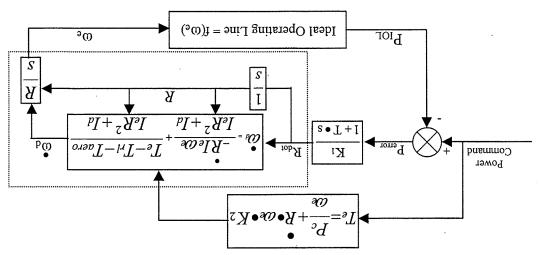


Figure 4.4.1. Power control algorithm

The dotted line box represents the system model, everything external to the box is the control system. The power command signal is derived from the accelerator pedal and sent through the summing junction. It is then subtracted from the ideal operating power to generate a power error signal. This error is multiplied by a gain, K1, and sent through a low pass filter with time constant T. The result of which is the Rdot command which dictates the shift rate of the transmission.

The Rdot command is then fed into the dynamic model of the CVT automobile. The equation used governs the acceleration of the drive shaft $\alpha_d = (\omega_d)'$. There are two terms to this equation. The second merely divides the net torque of the powertrain minus the road losses by the inertia of the vehicle to find the net angular acceleration. The first represents the torque produced by adding inertia to the powertrain by changing gear ratio of the transmission. This effect is normally burned up between clutch surfaces when shifting speeds in a discrete transmission. In the CVT, however, this term to decelerates or accelerates the car when shifting the transmission up or down.

The model then goes on to integrate all the necessary variables to keep track of the states in the system. These states are then fed into an ideal operating line for the powertrain. This line is a equation that represents the ideal power setting for each engine speed setting. Ideal meaning does not use this ideal line to control the power output, rather it uses this ideal power to generate an Rdot command the seek the point that is the most efficient for each power line.

Regardless of the ideal power output by the system IOL, the powertrain always provides

the desired power commanded through the top branch of the block diagram through two terms. The first term calculate the torque required by the powertrain by dividing the power command by the speed of the powertrain. The second term is compensating for the inertia discussed above term in the dynamic equation that governs the CVT.

Figure 5.4.2 shows how the control system transitions between two power lines.

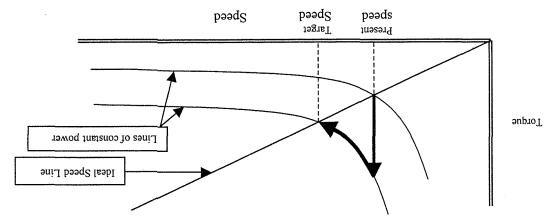


Figure 4.4.2. Power control transition between two power lines.

This control system works well at speeds above about 5 miles and hour. However, at low torque to satisfy the power setting. Although the computer simulation runs fine with these huge values of torque, the drivability of the automobile is not acceptable for human drivers.

$$\frac{p \partial \partial dS}{p \partial \partial dS} = \partial p \partial dS$$

At the low speeds where the high values of torque are an issue the transmission will always be in the lowest possible gear. This feature allows the control system to command a torque proportional to the accelerator command for low speeds. This control policy does not allow for optimization of power transfer at extremely low speeds, however, once the cutoff speed is reached for power control, the system then transitions from torque control and begins shifting the transmission to seek the highest efficiency under power control.

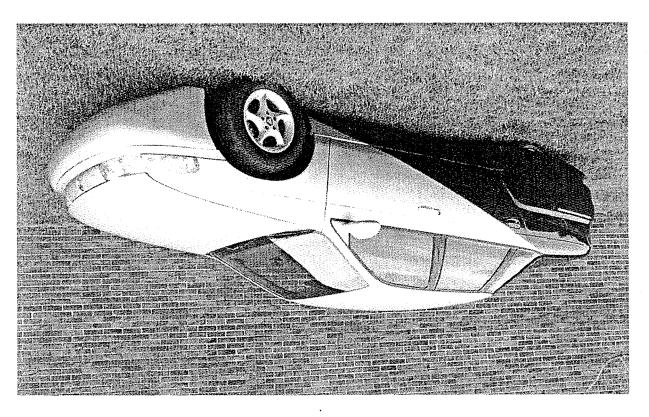
Phase 2 5.0 Introduction

Phase I proved the proposed control method can robustly control a CVT in an effective and efficient manner. Phase 2 will incorporate the knowledge that was learned in Phase I and apply it in a real vehicle application. The vehicle chosen for this CVT research was the 1999 UC Davis Future Car. It is a parallel hybrid electric vehicle that is base on a Mercury Sable chassis. The main goals for the Future Car are high fuel economy and low emissions. These goals

parallel the long term goals of this CVT research.

Up to this point, UC Davis had assembled and tested a prototype servo-hydraulic CVT on a dynamometer. This paper explores the integration and use of a similar CVT in a vehicle.

a dynamometer. This paper explores the integration and use of a similar CVT in a vehicle. Shown below is the 1999 UC Davis Future Car.



The 1999 UC Davis Future Car CVT Parallel Hybrid Electric Vehicle

quis2 0.0

The CVT, which was tested on the dynamometer, was a Nissan 1.0L class CVT which can transmit a maximum of 110 Mm of torque. For this vehicle application a much larger amount of torque capability is required. A larger CVT is therefore required. The selection of a CVT was limited by available production CVT's. Transmissions produced by Honda, Volvo, and Nissan were investigated for use in this vehicle. Table 6.1 shows the maximum allowable torque of each transmission, which indicates its acceleration potential.

Table 6.1 Torque capacities of production CVT's.

mN 062	7.0 L	Nissan
mV 241	1.8.L	OVIOV
mV 041	191	Honda
Maximum Allowable Torque	Class	

The Nissan CVT was chosen because of its high maximum allowable torque. This Nissan CVT although twice as large, has numerous similarities to the 1.0L CVT. This CVT is used as part of a parallel hybrid electric powertrain, which combines the torque from an electric motor and a gasoline engine. Shown below is the belt and pulleys of the 2.0L CVT.

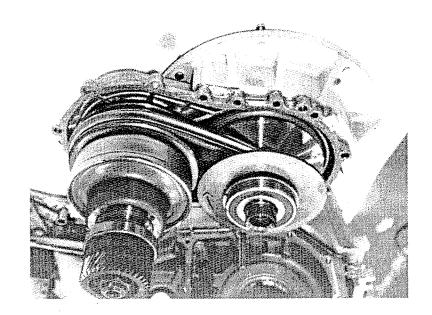
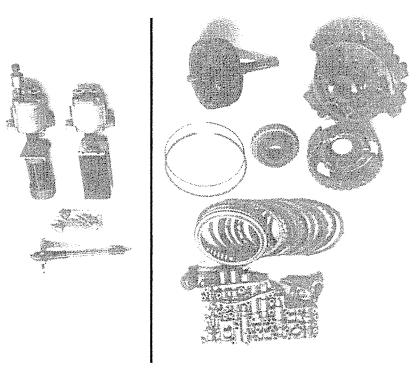


Figure 6.1 – 2.0L CVT Belt and Pulley System

The Nissan CVT was modified to improve efficiency and mechanical simplicity. Mechanical changes to the CVT were made with the goal of removing unnecessary components while retaining as many stock components as possible. The bi-directional rotation of the electric motor allowed removal of the reversing gear set and all of its related clutch components. The electric motor's high torque at low speed and the lack of engine idle in the control strategy eliminated the need for a torque converter. These modifications reduce the mechanical complexity of the CVT and the operating losses caused by these systems.

Figure 6.2 shows the high pressure pump, shifting valve body, and reversing components removed from the Missan CVT and the equivalent replacements added by UC Davis. And in Figure 6.3 the stock Missan valve body is shown on the left were as on the right is the UC Davis servo motor \ pump system.



UC Davis Components

Nissan Components
Figure 6.2 - Internal CVT mechanical simplification.

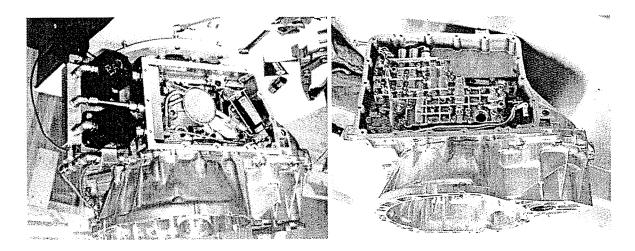


Figure 6.3 - The Vissan Mechanical Hydraulic Control System (left)

The custom UC Davis Computer Controlled Servo Hydraulic System (right)

The 1999 UC Davis Future Car uses an in-line, CVT, parallel hybrid powertrain which integrates the latest technologies in internal combustion engines, transmissions, and electric

motors. A small displacement engine and permanent magnet brushless DC motor are directly coupled to the CVT. A diagram of the powertrain is shown in Figure 6.4. This powertrain design focuses on simplicity, reliability, and manufacturability

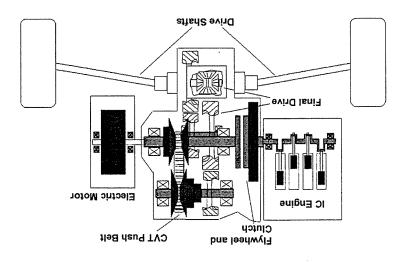


Figure 6.4 - In-line Powertrain Schematic.

The Nissan CVT has only one torque input which is from the engine. For use in this hybrid powertrain system it had to be modified. An internal spline was machined into the end of the input pulley. This spline was used to couple the electric motor to the transmission. This spline was design for an operating life greater than 10,000 miles, if machining and assembly error are less than 0.005 inches. A custom bracket was also manufactured to mount the motor to the case.

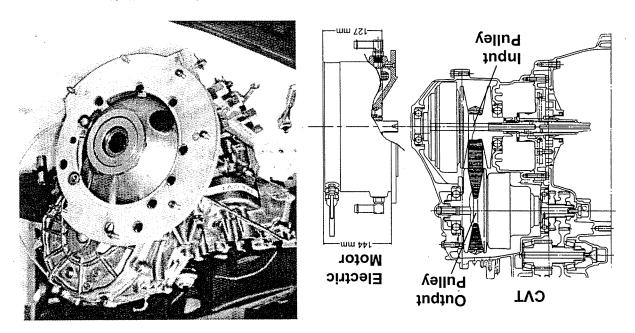


Figure 6.5 - Drawing of Cross Section of the CVT and the Electric Motor (left)

Custom Electric Motor Mount and View of Internal Spline (right)

The stock hydraulic pump and electro-mechanical control system were replaced with computer controlled servo hydraulic pumps to improve system efficiency. The stock Nissan pump is sized to supply the flow rate required for shifting, lubrication, and maximum clamping pressure at low engine speeds. As engine speed increases, the flow rate from the positive displacement pump increases. Clamping pressure depends only on torque demand instead of engine speed, so pressure is maintained by increasing fluid bleed-off. Therefore, system losses increase with engine speed. The servo hydraulic pump system decouples flow rate from engine speed. This system provides the necessary flow rate and clamping pressure on a demand basis with no bleed-off. Initial testing has shown an order of magnitude reduction in parasitic hydraulic losses. The hydraulic circuit including pumps, pressure regulators, and pressure transducers is shown in Figure 6.6.

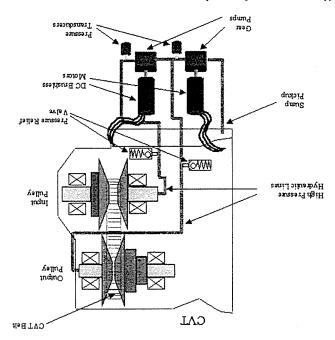


Figure 6.6 CVT hydraulic system schematic.

7.0 Results

After months of designing, manufacturing, and assembly, the CVT was successfully implemented into the 1999 UC Davis Future Car. The vehicle was driven from Detroit to Washington D.C. The calculated fuel economy for this trip was 40 mpg which is a considerable increase over the stock vehicle's fuel economy. The control system for the CVT proved to be very robust in many operating conditions such as extremely hot weather, hill climbing, and even performance driving. Operation of the CVT was incredibly smooth and seamless as it shifted, and noise levels were very low even when transmitting high torque and high power.

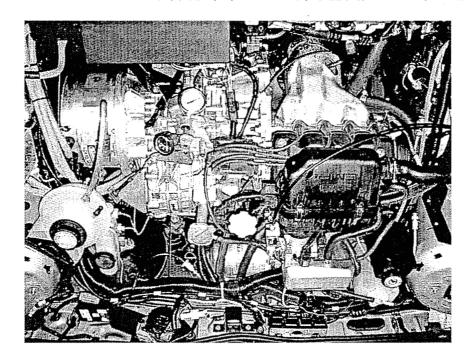
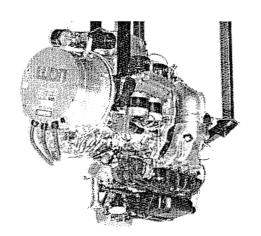


Figure 7.1 - CVT Parallel Hybrid Powertrain in Vehicle



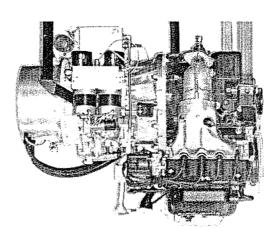


Figure 7.2 - CVT Parallel Hybrid Powertrain

8.0 Conclusion

The concepts proposed by Dr. A. Frank and Gear Chain Industrial are feasible and servohydraulic control of a CVT has been implemented. The advantages of such an implementation are increased energy efficiency, and precise control of the powertrain power loading.

The closed loop control algorithms needed for a servo-hydraulic CVT have been

demonstrated. It is necessary to incorporate non-linear control concepts into these control algorithms to achieve the desired performance, however, with the availability of high performance, low cost, digital controllers today, the concepts are simple and inexpensive to implement.

The replacement of a discrete transmission with a CVT necessitates a control structure to take advantage of the new gear ratio flexibility. One such structure has been designed and simulated which uses the CVT to move to powertrain to its point of highest efficiency for each power setting commanded by the driver. This strategy could be easily modified to optimize fuel economy or exhaust emissions.

The 1999 UC Davis Future Car has proven that a CVT hybrid powertrain can greatly improve fuel economy. Further improvements though can be made in continued optimization of powertrain control strategies and energy management.

9.0 References

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- 8. M. Alexander, B. Johnston, D. Funston, B. Huff, R. Carlson, N. Meyr, B. Moran, B. Momsen, Dr. A. Frank, and Dr. A. Burke; "A Mid-Sized Sedan Designed for High Fuel Economy and Low Emissions: The 1999 UC Davis Future Car "University of California at Davis, Dept. of Mechanical Engineering, Davis, California.

```
shared int dij,
            // interupt counter
                                                ![8] ano
                                                  D[3]'
                                                  '[8]I
                                                  '[E]d
                                         cmd_filter[3],
                                          fb_filter[3],
                                            command[3],
                                          feed_back[3],
                                              GLLOL[3] '
                                          analog_read2,
                                          analog_readl,
                                                             shared float analog_read0,
                                           // Global Variable accessable to interupts
                                                                 #define Sat_pos (4.0)
                                                                 #define Sat_neg (-4.0)
                                                            #define sample_time 0.00333
                                                                     #define wc_cmd 1.0
                                                                    #define wc_fb 188.0
                                                                       #define Ksh 1.50
                                                                        #define Kff 1.0
                                                                         #define Kd 0.0
                                                                      #define Kp 0.350
#define Ki 1.0
                                                                      //#define Ki 60.8
                                                                      //#define Kp 4.83
                        ZH 008 ~ 0L08 //
     // 0500 ~100 #e00 ~500 T8#00 ~ 20
            Des 010.*(02 \ zHMSp.81) \\
       // before actuating the routine
                                                                    #define ticks 3070
   // the number the counter counts to
           // PRT chl interrupt enable
                                                                          #qetine TIE1 5
                                                                  #define PRT1 VEC 0x06
           // PRT Chl down count enable
       // interrupt vector for timer l
                                                                           #define OFF 0
                                                                           #define ON 1
                                                                    // Define Constants
                                                                void init_global(void);
                                                               int PRT1 init( int to);
int DAC2 init( void );
                                     // Initialize PRT1
                                                                 // function prototypes
                                                                      #use ezioplc2.lib
                                                                      #use eziopbdv.lib
                                                                      #use eziocmmn.lib
                                                                      #use eziobll7.llib
                                                               // Sample rate = 300 Hz.
// This program is a digital feedforward compensator for the clamping pressure loop.
```

Sample pressure control compensator

A xibnəqqA

```
error[0] = (analog_readl - fb_filter[0]) ;
                                                         // process the data, herein lies the compensator transfer function
                                                                          (1 - wc_cmd*sample_time) * cmd_filter[1];
                                                                                          cmd_filter[0] = wc_cmd*sample_time*command[1] +
                                                                                                                                                              ്രായത്മുർ [0]
                                                                                                                             = susjod_resq1 !
                                                                               (1 - wc_fb*sample_time) * fb_filter[1];
                                                                                         tp[j] = mc[j] = mc[j] +
                                                                                                                           teed_psck[0] = susjod_resq0;
                                                                                                             analog_read0 = analog_read0 - .23;
analog_read2 = analog_read2 - 1.697;
                                                          // remove sensor bias
                   // Sample analog channel 2
                                                                                                                                 analog_read2 = eioBrdAI(2);
                    // Sample analog channel l
                                                                                                                                 analog_readl = eioBrdAI(l);
                                                                                                                                 analog_read0 = eioBrdAI(0);
                    // Sample analog channel 0
                                                              // analog_read2 = current feed forward command signal from shift
                                                                                                          // susjod_resq1 = pressure command signal
                                                                                                      // susjod_resq0 = pressure feed back signal
                                                                                                                                                            // sustog read
                                                            // Interrupt code body:
                                                   f( do ;= f) return;
                    // hit the watch dog timer
                                                                                                                                                                      prtwd();
        // re-enable the interrupt again
                                                                                                                                                                           EI():
                                                      // Clear timer register
                                                                                                                                                      inport ( TMDR1L);
                                                                                                                                                            inport ( TCR);
                                                                          1/ 55
                                                                                                                                               interrupt reti rupt(){
                                                           // create a interrupt vector
                                                                                                                                              #INT_VEC PRT1_VEC rupt
                                                                                                                              // main() ==> PRT1_Init(ticks);
                                                                             // needs an initialization routine to be called first in
                                                                                                            // The count down timer interupt routine
                                                                                                                                                                                   {
                                                                                                                                                                                {
                                                                                                                                                                      :++T
                                                                                                                                                                      t = 0
                                                                          'susjod_read0, analog_read1, analog_read2);
                                                                                   "n/lt.3+%=SA lt.3+%=[0]INA lt.3+%0NA")ltnirq
                                                                                                                                                                                     //
                                                                          , out [0], error [0], P[0], I[0], analog_read2);
                                         "m/le.3+%=2A le.3+%=[0]lle.3+%=[0]fle.3+%=[0]e%+60luo")llie.3+%=340luo")llie.3+%=2A le.3+%=[0]lle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3+%=[0]fle.3
                                                                                   ,error[0],P[0],out[0],analog_read2);
                                                          "\pi/1E.3+%=SA 1E.3+%=[0] two 1E.3+%=[0] q 1E.3+%=[0] rorrs")liming
                                                                                                                                                       î ( ( i < 2 5 ) {
                                                                                                                                                                         in here.
 // like crazy
                                                                                                                                                              tnterrupt runs
                                                                                                                                                                     lor(;;){
           // outside loop of joy,
                                                                                                                                                                       3o = 1
                                                                                                                                                                      Pitwd();
                          // Hit Wd timer
                 // Initialize the analog in
                                                                                                                                                          eioBrdInit(0);
                                                                                    printf("Could not initialize the DAC2 board/n");
                                                                                                                                           if( DAC2_init() == -1)
                                           // initalize analog out board
                                                                                                                                                    init_global();pRT1_init(ticks);
// Initialize the count down timer interrupt
                                                                                                                                                                          :0 = I
                                                                                                                                                                          ţ عسب ؛
                                                                                                                                                                           }() rism
                                                                                                                                                                                 biov
             // For a triangular test wave
                                                                                                                               tjedt:
                                                                                                               analog_write,
                                          // value to be written to DAC2
                                         // DAC2 address number
                                                                                                                      DAC2_addr,
                                                                                                                          Ted_ctr,
                                                                                                                                                                  initialized!
   // do not start sampeling untill adc's are
                                                                                                                                                               'o5
```

```
iser( TCR, TDE1 );
                      // enable count down
                                     onfbort (RLDR1H, tc >> 8); //set reload counter
                                                                 onfborf (RDBIL) to
                                               // :(
                                      outport (TMDR1H, tc >> 8); //Set data register
                                                     '(
                                                                   onfbort (IMDRIL, tc
                             // Disable interrupts
                                                                  IRES ( TCR, TDE1 );
                     // Disable count down
               // tc = ticks defined above
                                                                   PRT1_init (int tc) {
                                                                                    ηuŗ
                                                                                      {
                                                                                    {
                                                               awifcpreD(J);
                                                                           } egge {
                                                                         dt\dot{J} = \dot{L}tb
                                                                swifchLED(0);
                                                                      if(dij == 0){
                                                                         Jed_ctr = 0;
                                                                  f( Jed_ctr >= 50) {
                                                                          Jed_ctr++;
                                               /* The human i/o portion of the deal: */
                                                                    onr[T] = onr[0]:
                                                                  // out[2] = out[1];
                                                                exxox[7] = exxox[0]
                                                              GLLOL[5] = GLLOL[7];
                                                      cmd_filter[1] = cmd_filter[0];
                                                    // cmd_filter[2] = cmd_filter[1];
                                                          command[1] = command[0];
                                                        ([1]);
                                                        tb filter[2] = fb filter[0];
tb filter[3] = fb filter[1];
                                                        teed_back[1] = feed_back[0];
                                                      // update the history variables just in case we need them later
                                                     // channel 2 = DAC2_addr +1
                                                            plcxP86Out(DAC2_addr,0);
// outpuut to DAC2 channel l
                                                     // channel 2 = DAC2_addr +1
                 plcxP86Out (DAC2_addr+1, analog_write*-1);// outpuut to DAC2 channel l
                                                     // cysuuel 2 = DAC2_addr +1
                                                          plcxP86Out(DAC2_addr+1,0);
// outpunt to DAC2 channel l
                                                    // channel 2 = DAC2_addr +1
                                                  plcxP86Out (DAC2_addr, analog_write);
              // outpunt to DAC2 channel l
                                                               if(analog_write >= 0) {
                                                                        // analog write
                                                    snalog_write = (out[0]) * 409.6;
                                                                  onf[0] = Sat_neg;
                                                                if(out[0] < Sat_neg) {
                                                                  onf[0] = Sat_pos;
                                                                if(out[0] > Sat_pos) {
                              out[0] = P[0] + analog_readl*Kff - analog_read2 * Ksh;
                                                               b[0] = exxox[0] * Kb
```

```
analog_read1 = 0.0;
                                                                              analog_read0 = 0.0;
                                                                                 command[2] = 0.0;
                                                                                 ;0.0 = [1] brammoo
                                                                                command[0] = 0.0;
                                                                             cmd_filter[1] = 0.0;
cmd_filter[2] = 0.0;
                                                                              cmd_filter[0] = 0.0;
                                                                              fb_filter[2] = 0.0;
                                                                               fb_t[t]tex[1] = 0.0;
                                                                               fb_filter[0] = 0.0;
                                                                              teed_back[2] = 0.0;
                                                                               feed\_back[l] = 0.0;
                                                                               feed_back[0] = 0.0;
                                                                                     0.0 = [2] = 0.0;
                                                                                     0.0 = [1] = 0.0
                                                                                     0.0 = [0] = 0.0
                                                                                      D[2] = 0.0;
                                                                                       D[T] = 0.0
                                                                                       0.0 = [0]
                                                                                       ;0.0 = [S]I
                                                                                       (0.0 = [1]]
                                                                                       ;0.0 = [0]I
                                                                                       p[s] = 0.0
                                                                                       b[T] = 0.0
                                                                                       0.0 = [0]
                                                                                   error[2] = 0.0;
                                                                                   error[1] = 0.0;
                                                                                   error[0] = 0.0;
                                                                                           do = 0
                                                                                            tjsspjeg
                                                                                     feg_{cfx} = 0:
     // the number of interrupts for
                                                                                          t_0 = t_{b}
// led flasher state variable
                                                                                   init_global (void)
                                                                                                 ρτολ
                                                                                                    {
                                                                              return (DAC2_addr);
                                                                                              egae
                                                                                   return -1 );
                                                                             if(DAC2_addr == -999)
                                               //printf("Channel located = %d/n",test_addr);
                                                                   DAC2_addr = test_addr * 2;
                                                             if(plcxP86Init(test_addr) != -l ) {
                                                                                   pitwd();
                                                for(test_addr = 0; test_addr <= 7; test_addr++){</pre>
                                                                                 DAC2_addr = -999;
                                                     // Locate XP8600s; Save the highest addr found
                                      //printf("I am so excited about salad sensations!!!/n/n");
                                                                           for (j=0; j<10; j++);
                                                                                         YTEMG():
                                  // ednalize
                                         for (i = 0; i <= 15000; i++) { // lag a bit so the DAC can
                                                                                          PŢĘMg() !
                                                 // xeser the plobus
                                                                                 ejoKesetPlcBus();
                                                                                          y;()pma;q
                                                                              int i, j, test_addr;
                                                                                    DAC2_init (void) {
                                                                                                    {
                                                                                         xernxu(0):
                                                                                              EI():
                                                                                ISET ( TCR, TIEL );
                                         // enable interrupts
```

analog_read2 = 0.0;
}

dither,

Sample Rdot Compensator Routine Appendix B

```
, Snise
                                                             gainl,
                                                              D[5]'
                                                              I[S]'
                                                             7[2]đ
                                                           ont[3],
                                                         6xxox[3]'
                                                        r_desired,
                                                     analog_readl,
                                                  analog_read0[2],
                                                                        spared double analog_read,
                                                       // Global Variable accessable to interupts
                                                                          #define sample_time 0.02
                                                                     #define derivitive_alpha 0.10
                                                                       #define integral_alpha 10.0
                                                                                 #define gain 18.0
                                                                                 #define r_max 4.9
                                                                                 #define r_min 4.3
                                                                               #define r_gain 12.0
                                                                           #define r_dot_gain 36.0
                                                                      #define r_derivitive_alpha 4
                                                                       #define r_derivitive_time l
                                                                         #define derivitive_time l
                                                                           #define integral_time 1
                   05 ~ 00#8T 00Z~ 009# 00T~ 00Z6 //
                     >>s 010.( * (02 \ ZHM12 \ ) \ )
                     // before actuating the routine
                        // the number the counter counts to
                                                                             #define ticks 4600
                                // PRT chl interrupt enable
                                                                                    #qefine TIEl 5
                                // PRT Chl down count enable
                                                                                    #define TDE1 1
                                                                             #define PRTL_VEC 0x06
                            // interrupt vector for timer l
                                                                                     #define OFF 0
                                                                                      #define ON l
                                                                               // Define Constants
                                                                         ;( biov )ladolg_ini biov
                                                                            int PRT1 init( int to);
int DAC2 init( void );
                                                 // Initialize PRT1
                                                                            // function prototypes
                                                                                 #use ezioplc2.lib
                                                                                 #use eziopbdv.lib
                                                                                 #use eziocmmn.lib
                                                                                 #use eziobll7.lib
                                                                                              . amit
// Samples data at 200 Hz. But only processes the data at 50 Hz. Averages 4 //analog reads each
                                                                               // Rdot controller.
```

```
\ hit the watch dog timer
                                                                                                                                                                                                         p;cmg():
  // re-enable the interrupt again
                                                                                                                                                                                                                 EI():
                                                            // Clear timer register
                                                                                                                                                                                      inport ( TMDR1L);
                                                                                      1/ 55
                                                                                                                                                                                             inport ( TCR);
                                                                                                                                                                            #INT_VEC PRT1_VEC rupt(){
                                                                   // create a interrupt vector
                                                                                                                                                         // main()==> PRT1_Init(ticks);
                                                                                         // needs an initialization routine to be called first in
                                                                                                                                 // The count down timer interupt routine
                                                                                                                                                                                                                                {
                                                                                                                                                                                                                           {
                                                       printf("a0 %f al %f e0 %f/n", analog_read0, analog_read1, error[0]); \\
                                                                                                                                                                                                          t = 0
                                                analog_readl, r_dot[0], analog_read0[0], P[0], hold, D[0], out[0]);
                                  out[0],P[0],analog_readl,error[0],analog_read0[0],r_dot[0]);

yantl("RD_c=%e_3f rdot=%e_3f R.3%=y 18.3%=y 18.3%=out=%e_3f/n",

yantl("RD_c=%e_3f rdot=%e_3f R.3%=y 18.3%=y 18.
                                                 printf("out[0]=%.3f P=%.3f RD_c=%.3f Err= %.3f R=%.3f rdot=%.3f\n",

Ţ[(Ţ<S2) {</pre>
                           // Jike crazy in here.
                                                                                                                                                                                                                        sunz
                                                                                                                                                                                                          lox(::){
// outside loop of joy, interrupt
                      // initialize interrupt counter
                                                                                                                                                                                                          dij = 0;
                                                             // HIE Wd timer
                                                                                                                                                                                                          y;cmq():
                                eioBrdInit(0);
                                                                                                                                                                        if( DAC2_init() == -1)
                                              // initalize analog out board
                                                                                                                                                                                   PRT1_init(ticks);
         // Initialize the count down timer interrupt
                                                                                                                                                                                                   ; () GIa_tini
                                                                                                                                                         // Initialize PID coefficients
                                                                                                                                                                                            init_global();
                                                                                                                                                         // Initialize global variables
                                                                                                                                                                                                              :0 = T
                                                                                                                                                              // Initalize local variables
                                                                                                                                                                                                               int it;
                                                                                                                                                                                                                 }()uism
                                                                                                                                                                                                                        DTOA
                                                                                                                                                          pojq:
                                                                                                                                                    avg_ctr,
         // For a triangular test wave
                                                                                                                                                           fjed)
                                                                                                                                       analog_write,
                                               // value to be written to DAC2
                                             // DAC2 address number
                                                                                                                                               DAC2_addr,
                                                                                                                                                                                               shared int dij,
                                                    // interupt counter
                                                                                                             susjod_resq0_old;
                                                                                                                                  x_dot[2],
                                                                                                                                    al_temp,
                                                                                                                                    a0_temp,
                                                                                                                                             KD₫'
                                                                                                                                              въз у
                                                                                                                                             KDS,
                                                                                                                                              кол,
                                                                                                                                                Dđ '
                                                                                                                                               D3 '
                                                                                                                                               DS'
                                                                                                                                               DJ'
                                                                                                                                                'bI
                                                                                                                                               'EI
                                                                                                                                                'ZI
                                                                                                                                                'TI
                                                                                                                                               'ta
       // PID calculation coeficients
```

```
\dot{\epsilon} (0.0 > [0] \dot{\epsilon} win \dot{\epsilon} out [0] out out (0.0 > 10) \dot{\epsilon}
                                                        it(analog_read0[0] > r_max && out[0] < [0] out [0] = 1
                                                                                                                                          ;29.4- = [0] = (29.4- > [0] = (29.4- > [0] = (29.4- > [0] = (29.4- > [0] = (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- > (29.4- 
                                                                                                                                                $1.56.4 = [0]$ and ($6.4 < [0]$ and) $$1.56.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.4 = $1.86.
                                                                                                                                      out[0] = P[0] * r_dot_gain - 0.6;
                                                                                                                                                                                              b[0] = exxox[0]
                                                                                                           error[0] = analog_readl * 1.6 - r_dot[0];
                                                                                              \ (r_derivitive_alpha+sample_time);
                                                                                                               + r_dot[l]*r_derivitive_alpha
                                                                                                  rdor[0] = (analog_read0[0] - analog_read0[1]
                                                                                                                                                                                                                    yogg = 0
                                                                                                                                                                                                                                        ejse{
                                                                                              \(r_derivitive_alpha+sample_time);
                                                                                                             + r_dot[1]*r_derivitive_alpha
                                                                                                 r_dot[0] = (analog_read0[0] - analog_read0[1]
                                                                                                                                                                        out[0] = P[0] * r_gain;
                                                                                                                                                                                              b[0] = exxox[0]:
                                                                                                                  error[0] = r_desired - analog_read0[0];
                                                                                                               it(r_desired < r_min) r_desired = r_min;</pre>
                                                                                  r_desired = r_desired + analog_readl*sample_time;
                                                                                                                                                r_desired = analog_read0[0];
                                                                                                                                                                                                              pojq = 5;
                                                                                                                                                                                                     if(hold :=2 ){
                                                                              )( 0 => fbear_polene && so.o- < tbear_polene)li sele
                                                                                              \(r_derivitive_alpha+sample_time);
                                                                                                              + r_dot[1]*r_derivitive_alpha
                                                                                                  x = (9) = (9) = (9) = (9) = (9) = (9) = (9)
                                                                                                                                                                        ont[0] = P[0] * r_gain;
                                                                                                                                                                                              b[0] = GRIOR[0]:
                                                                                                               if(r_desired > r_max) r_desired = r_max;
error[0] = r_desired - analog_read0[0];
                                                                                  r_desired = r_desired + analog_read1*sample_time;
                                                                                                                                                r_desired = analog_read0[0];
                                                                                                                                                                                                              yorg = r'
                                                                                                                                                                                                     f(hold !=1){
                                                                                                     if (analog_read1 < 0.03 && analog_read1 > 0) {
                                      if ( analog_read1 > 0.01 && analog_read1 < 0) analog_read1 = 0;
                                             :0 = [bear_polene (0 < lbear_polene && 10.0 > tbear_polene )li
                                                          analog_read1 = (analog_read1 - 2.5) / 15; // Rdot command
                                                                                                                                     // analog_read1 = ratio command signal
                                                                                                                               // analog_read0 = ratio feed back signal
                                         // process the data, herein lies the compensator transfer function
                                                                                                                                                                                                               9AA^{-}ccx = 0
                                                                                                                                                                                                               90^{\circ} ceub = 0:
                                                                                                                                                                                                               al_temp = 0;
                                                                                                                                                susjod_resq1 = s1_remp/svd_ctr;
                                                                                                                                      susjod_read0[0] = a0_temp/avg_ctr;
                                                                                                                                                                                                                                            else{
                                                                                                                                                                                                                               :uznqəz
                                                                                                                                                                                                                     gad cfr++;
// Sample analog channel 1
                                                                                                                                                                           al_temp += eioBrdAl(1);
// Sample analog channel 0
                                                                                                                                                                          a0_temp += eioBrdAl(0);
                                                                                                                                                                                                     if(avg_ctr < 3){
                                                             // snalog read, average four sample values for error removal
                                                                                                                                                                                 *********//
                                                                                                                                                                                    // Interrupt code body:
```

```
for (j=0; j<10; j++);
                                 // equalize
                                                                                      YŢĘMG():
                                       for (i = 0; i < = 15000; i++) { // lag a bit so the DAC can
                                                                                        prtwd();
                                               // xeser rpe brcpns
                                                                              eioResetPlcBus();
                                                                                        /() pmqtq
                                                                           tuf i, j, test_addr;
                                                                                 DAC2_init (void) {
                                                                                                 {
                                                                                      xernxu(0):
                                                                                           EI():
                                                                             ISET ( TCR, TIEL );
                                       // enable interrupts
                                                                             ISEL ( LCK' LDET );
                               // enable count down
                                               //ser reload counter
                                                                     onfborf(RLDRlH, tc >> 8);
                                                                             onfborf (RLDRil, tc
                                                               : (
                                                        //
                                               outport (TMDR1H, tc >> 8); //Set data register
                                                               : (
                                                         //
                                                                             onfborf (IMDRIL, tc
                                                                             IKES ( TCR, TIEL );
                                      // Disable interrupts
                              // Disable count down
                                                                              PRT1_init ( int tc) {
                        // tc = ticks defined above
                                                                                               дит
                                                                                                 {
                                                                                               {
                                                                                       qrJ = 0
                                                                      BankX(Y), Y = bit of port X
   //
                       // I is for on, 0 is for off
                                                                       eioBrdDO(BankB(0), 0);
                                                                                       } egra {
                                                                                      t = fp
                                                                      Bankx(Y), Y = bit of port X
   .//
                       // I is for on, 0 is for off
                                                                       ejoBrdDO(BankB(0), 1);
                                                                                  ) (0 == [ib) li
                                       /* The human i/o portion of the deal: DIO for the scope*/
                                                            suslog_read0[1] = analog_read0[0];
                                                                          x = x = x = x[0]
                                                                           = D[0]
                                                                                          ſτlα
                                                                    ![0]I =
                                                                                           [T]I
                                                                    = b[0]:
                                                                                           b[I]
                                                                         = ont[0];
                                                                                        ont[1]
                                                                          \operatorname{error}[\mathfrak{I}] = \operatorname{error}[\mathfrak{0}]
                                                                  // update the history variables
                                       // channel 2 = DAC2_addr +1
                                                    plcxp860ut(DAC2_addr + l,analog_write);
       // outpunt to DAC2 channel l
                                        // channel 2 = DAC2_addr +l
                                                                                    DAC2 channel 1
                                                                 plcxP86Out(DAC2_addr , 0 );
// onfpunt to
                                                if(analog_write > 3686) analog_write = 3686;
                                                           analog_write = (out[0]) * (-205);
                                                                    else { // must be negative
                                        // cysuuel 2 = DAC2_addr +l
// outpunt to DAC2 channel 1
                                                         plcxP86Out (DAC2_addr, analog_write);
                                        cpsuuej j
                                                               plcxP86Out(DAC2_addr + 1, 0);
   // outpunt to DAC2
                                                if(analog_write > 3686) analog_write = 3686;
                                                          analog_write = (out[0]) * (205);
                                                                              }(0.0 < [0] tuo)lt
```

```
tpT + rT = rd
                                                                                      K = \text{dayu}
                                                                   Alpha_d = derivitive_alpha;
                                                                     Alpha_i = integral_alpha;
                                                                              Ts = sample_time;
                                                                          Td = derivitive_time;
                                                                           Ti = integral_time;
                                                                   ....tI'ta'ta//
                                                                     biD's are declared globally
//
                       Ti, Td, Ts, Alpha_i, Alpha_d, K; // calculation variables.
                                                                            controler coefficient
   did //
                                                                                           float
                                                                                  } (biov) dId_tini
                                                                                              ptov
                                                                                  x = qor[T] = 0
                                                                                  x = qor[0] = 0
                                                                          suslog_read0_old = 0;
                                                                                      yojg = 0:
                                                                                   31_temp = 0
                                                                                   softenb = 0:
                                                                                   gAG^{c}ccc = 0
                                                                                  dither = 1.0;
                                                                            :0.0 = Lbear_golena
                                                                         analog_read0[1] = 0.0;
                                                                        analog_read0[0] = 0.0;
                                                                             analog_read = 0.0;
                                                                                    0.0 = [I] = 0.0
                                                                                    0.0 = [0] \alpha
                                                                                    :0.0 = [I]I
                                                                                    ;0.0 = [0]I
                                                                                    :0.0 = [I]q
                                                                                    p[0] = 0.0
                                                                                  0.0 = [2] ano
                                                                                  0.0 = [1] = 0.0;
                                                                                  0.0 = [0] = 0.0
                                                                                error[2] = 0.0;
                                                                                exxox[7] = 0.0
                                                                                error[0] = 0.0;
                                                                               r_desired = 0.0;
                                                                                     gain2 = 1;
                                                                                   ; c4. = faise
                                                                                      ig = 0:
                                                                                          t_0 = x
                                                                                                 }
                                                                                init_global (void)
                                                                           return(DAC2_addr);
                                                                                           ejze
                                                                                xernxu( -1 ):
                                                                          if(DAC2_addr == -999)
                                           //printf("Channel located = %d/n", test_addr);
                                                         if(ploxP86Init(test_addr) != -1){
DAC2_addr = test_addr * 2;
                                                                                pitwd();
                                            for(test_addr = 0; test_addr <= 7; test_addr++) {</pre>
                                                                              DYCS gqqx = -888;
                                                // rocate XP8600s; Save the highest addr found
                                  //printf("I am so excited about salad sensations!!!/n/n");
```

{

Appendix C

Specification of the servomotor / pump characteristics for a CVT clamping pressure and shift

system.

Medium: CVT Traction Fluid (Similar to ATF) 1. General:

Temperature Range: -40°C min, +140°C max

2. Clamping Pump

35 bar (508 psi) Max Pressure:

(nim/snollsg 40.264 gallons/min) \pm Flow:

Displacement: 0.2 cm 3 / rev (0.01 sin 3 /rev) mgr 0009 Max speed:

3. Shift Pump

 \pm 20 bar (290 psi) Pressure:

(nim/snollsg $66.0\pm$) nim/retilč. $2\pm$ Flow:

mgr 0003 Max speed:

Displacement: 0.5 cm 3 / rev (0.03 l in 3 /rev)

The above characteristics led to the selection of these specific components for the actual

system:

6000 rpm Max speed: dmu4 Hid2 Max Pressure: 207 bar (3000psi) 1. Clamping Pump,

Displacement: 1.0 cm 3 /rev (0.0) very 5 mo (1.1)

Manufacturer: John S. Barnes

2. Clamping Servo, (dI•ft 9.11) m•N 2.91 Peak torque:

4000 rpm Max speed: Shift Servo (dl•ft 8.0) m•N82.1 Continuous:

Manufacturer: Hathaway Motion Control

with zero cogging torque available. problem for the control of the system. Hathaway motion control has many lines of servomotors for the motor. This was overlooked during the first design iteration and proved to be a serious In addition to the above characteristics, it is important to specify a zero cogging torque

Appendix D Development of CVT Dynamic Equations

Dr. Andrew Frank first developed this equation, which governs the torque produced by a vehicle dynamics for all driving cycle simulations.

$$\frac{-\dot{\mathbf{k}}\mathbf{I}_{e}\omega_{e} + \mathbf{T}_{e}\mathbf{R} - \mathbf{T}_{rl} - \mathbf{T}_{loss}}{(\mathbf{I}_{e}\mathbf{H}^{2} + \mathbf{I}_{d})} = \dot{\omega}_{d}$$

This equation is developed by looking at a free body diagram about the drive shaft of the vehicle model shown in figure D.1.

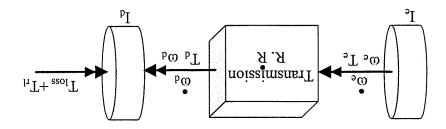


Figure D.1. CVT vehicle model.

The differential of the transmission is omitted for simplicity. It and Id are inertias in the system. It represents the engine or electric motor inertia, and Id represents the mass of the car transformed into a rotating inertia about the drive shaft $(m \cdot R_{wheel}^2)$.

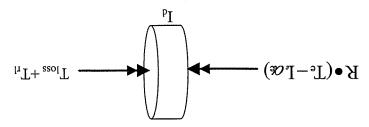


Figure xx. Free Body Diagram at the drive shaft.

Summing the torques and setting them equal to $I \bullet \alpha$ yields:

$$T_{\rm e}R-RI_{\rm e} c_{\rm e}-T_{\rm rl}-T\log s= I_{\rm d} c_{\rm e}$$
 Only,
$$\omega_{\rm e}=R\omega_{\rm d} \quad \omega_{\rm d}=R\omega_{\rm d}+R\omega_{\rm d}$$

Substituting the chain rule result into the above equation with a little massaging gives:

$$(\mathrm{Id} + \mathrm{Ie}R^2)_{\partial \mathcal{U}} = \mathrm{Te}R - \mathrm{kIe}_{\partial \mathcal{U}} - \mathrm{TrI} - \mathrm{Tloss}$$

Dividing both sides by the inertia term yields the dynamic equation used for our vehicle

This equation behaves somewhat intuitively, a positive electric motor or engine torque simulations.

gives a positive acceleration. And road load and aerodynamic losses

in a negative Rdot term which would actually increase the powertrain torque to the wheels! one is braking the power demand decreases and engine speed typically slows down. This results the drive shaft creates a somewhat counter intuitive feel to an automobile. For instance, when and a negative shift rate will contribute a positive acceleration to the drive shaft. This effect on such that the Rdot term is positive will contribute a negative acceleration effect to the drive shaft,

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This equation has somewhat surprising consequences. It states that shifting the CVT